

SIMATIC S7-1500, CPU 1511-1 PN, Central processing unit with working memory 150 KB for program and 1 MB for data, 1. interface: PROFINET IRT with 2 port switch, 60 NS bit-performance, SIMATIC memory card necessary



General information	
Product type designation	CPU 1511-1 PN
HW functional status	FS03
Firmware version	V2.8
Product function	
<ul style="list-style-type: none"> <li>I&amp;M data</li> <li>Isochronous mode</li> </ul>	<p>Yes; I&amp;M0 to I&amp;M3</p> <p>Yes; Distributed and central; with minimum OB 6x cycle of 625 µs (distributed) and 1 ms (central)</p>
Engineering with	
<ul style="list-style-type: none"> <li>STEP 7 TIA Portal configurable/integrated as of version</li> </ul>	V16 (FW V2.8) / V15 (FW V2.5) or higher; with older TIA Portal versions configurable as 6ES7511-1AK01-0AB0
Configuration control	
via dataset	Yes
Display	
Screen diagonal [cm]	3.45 cm
Control elements	
Number of keys	8
Mode buttons	2

Supply voltage	
Type of supply voltage	24 V DC
permissible range, lower limit (DC)	19.2 V
permissible range, upper limit (DC)	28.8 V
Reverse polarity protection	Yes
Mains buffering	
<ul style="list-style-type: none"> <li>• Mains/voltage failure stored energy time</li> </ul>	5 ms
<ul style="list-style-type: none"> <li>• Repeat rate, min.</li> </ul>	1/s
Input current	
Current consumption (rated value)	0.7 A
Current consumption, max.	0.95 A
Inrush current, max.	1.9 A; Rated value
$I^2t$	0.02 A <sup>2</sup> ·s
Power	
Infeed power to the backplane bus	10 W
Power consumption from the backplane bus (balanced)	5.5 W
Power loss	
Power loss, typ.	5.7 W
Memory	
Number of slots for SIMATIC memory card	1
SIMATIC memory card required	Yes
Work memory	
<ul style="list-style-type: none"> <li>• integrated (for program)</li> </ul>	150 kbyte
<ul style="list-style-type: none"> <li>• integrated (for data)</li> </ul>	1 Mbyte
Load memory	
<ul style="list-style-type: none"> <li>• Plug-in (SIMATIC Memory Card), max.</li> </ul>	32 Gbyte
Backup	
<ul style="list-style-type: none"> <li>• maintenance-free</li> </ul>	Yes
CPU processing times	
for bit operations, typ.	60 ns
for word operations, typ.	72 ns
for fixed point arithmetic, typ.	96 ns
for floating point arithmetic, typ.	384 ns
CPU-blocks	
Number of elements (total)	2 000; Blocks (OB, FB, FC, DB) and UDTs
DB	
<ul style="list-style-type: none"> <li>• Number range</li> </ul>	1 ... 60 999; subdivided into: number range that can be used by the user: 1 ... 59 999, and number range of DBs created via SFC 86: 60 000 ... 60 999

• Size, max.	1 Mbyte; For DBs with absolute addressing, the max. size is 64 KB
<b>FB</b>	
• Number range	0 ... 65 535
• Size, max.	150 kbyte
<b>FC</b>	
• Number range	0 ... 65 535
• Size, max.	150 kbyte
<b>OB</b>	
• Size, max.	150 kbyte
• Number of free cycle OBs	100
• Number of time alarm OBs	20
• Number of delay alarm OBs	20
• Number of cyclic interrupt OBs	20; With minimum OB 3x cycle of 500 µs
• Number of process alarm OBs	50
• Number of DPV1 alarm OBs	3
• Number of isochronous mode OBs	2
• Number of technology synchronous alarm OBs	2
• Number of startup OBs	100
• Number of asynchronous error OBs	4
• Number of synchronous error OBs	2
• Number of diagnostic alarm OBs	1
<b>Nesting depth</b>	
• per priority class	24
<b>Counters, timers and their retentivity</b>	
<b>S7 counter</b>	
• Number	2 048
<b>Retentivity</b>	
— adjustable	Yes
<b>IEC counter</b>	
• Number	Any (only limited by the main memory)
<b>Retentivity</b>	
— adjustable	Yes
<b>S7 times</b>	
• Number	2 048
<b>Retentivity</b>	
— adjustable	Yes
<b>IEC timer</b>	
• Number	Any (only limited by the main memory)
<b>Retentivity</b>	
— adjustable	Yes