Table 10A.V
Digital I/O Node Address Setting

Node Address		Switch Assembly Position					
Hexadecimal	**Binary**	**1**	**2**	**3**	**4**	**5**	**6**
00	00 0000	OFF	OFF	OFF	OFF	OFF	OFF
01	00 0001	OFF	OFF	OFF	OFF	OFF	ON
02	00 0010	OFF	OFF	OFF	OFF	ON	OFF
.	.	.	.	.	.	.	.
.	.	.	.	.	.	.	.
.	.	.	.	.	.	.	.
3D	11 1101	ON	ON	ON	ON	OFF	ON
3E	11 1110	ON	ON	ON	ON	ON	OFF
3F	11 1111	ON	ON	ON	ON	ON	ON

10A.4 High-density I/O Module

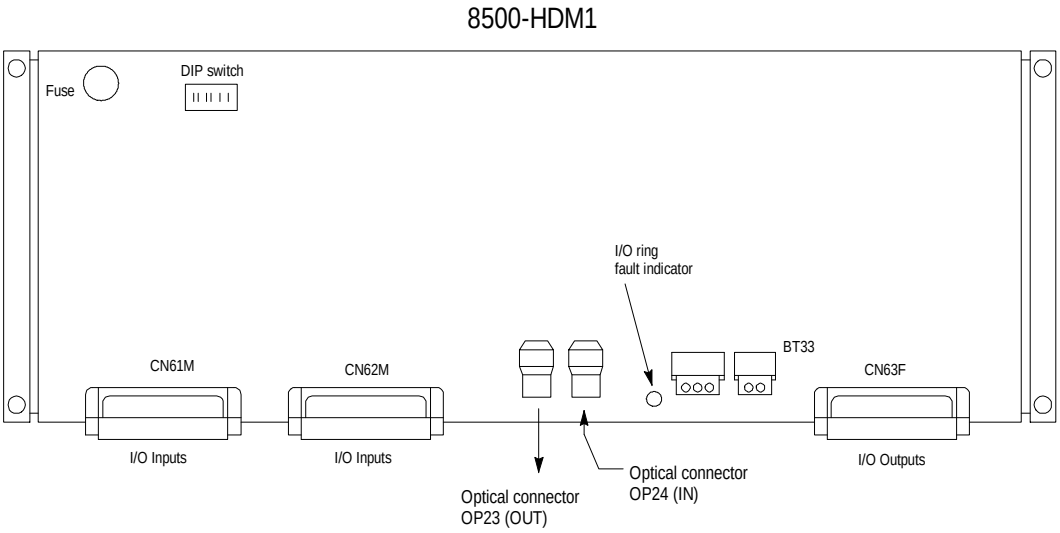
The high-density I/O module provides for an additional 66 inputs and 36 outputs to the system I/O ring.

The high-density I/O module receives input signals from external devices assigned to its input terminals. These signals are sent to PAL, through the I/O ring, to be used in the ladder logic process. PAL generates signals that are then sent through the I/O ring to the high-density I/O module. The high-density I/O module outputs these signals to external devices assigned to its output terminals.

This section covers the specifications, connection and the settings of the high-density I/O module (cat. nos. 8500-HDM).

Figure 10A.26 shows the high–density I/O module.

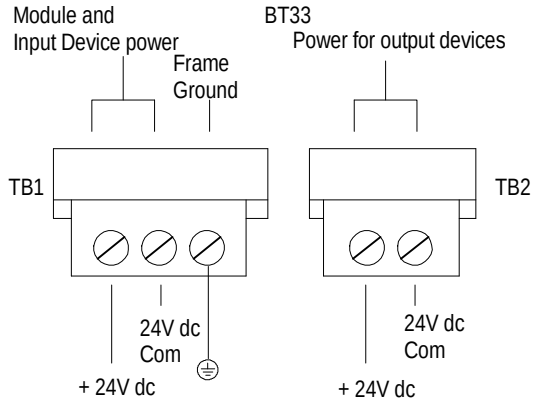
Figure 10A.26
High-density I/O Module



10A.4.1 High Density I/O Module Connection

Figure 10A.27 shows the power connections for the high–density I/O module.

Figure 10A.27
High-density I/O Module Power Connections



Important: The + 24V dc power for the high–density I/O module and its output devices may originate from the same power source. However, power sources with excessive noise or poor voltage regulation should not be used to power the high–density I/O module.

Figure 10A.28 shows typical input device connections to connectors CN61M or CN62M of the high–density I/O module.

Figure 10A.28
High-density I/O Module Input Device Connection

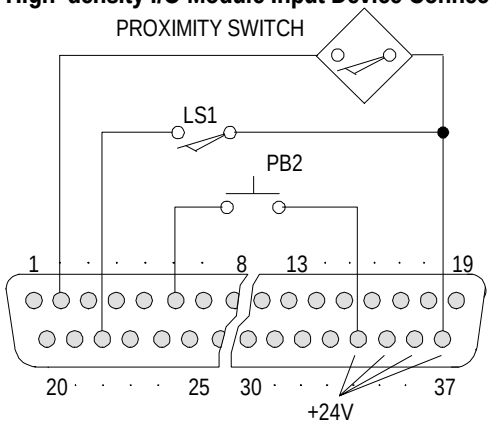


Table 10A.W and Table 10A.X show the relationship between the input data and the pins of connectors CN61M and CN62M.

Table 10A.W
Relationship of Input Data to Connector CN61M Pins

Connector CN61M					
Input Data	**Connector Pin No.**	**Input Data**	**Connector Pin No.**	**Input Data**	**Connector Pin No.**
D1	1	D12	12	D23	23
D2	2	D13	13	D24	24
D3	3	D14	14	D25	25
D4	4	D15	15	D26	26
D5	5	D16	16	D27	27
D6	6	D17	17	D28	28
D7	7	D18	18	D29	29
D8	8	D19	19	D30	30
D9	9	D20	20	D31	31
D10	10	D21	21	D32	32
D11	11	D22	22	D33	33

Important: Pins 34, 35, 36, and 37 are a fused +24 volt power used for input device circuits. If you chose to use an external power source to power input device circuits, do not make connections to these four pins. Additionally any external power supply used for input devices must have its common tied to the same potential as the common at TB1.